

EDUCATION

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| Northwestern University | Evanston, IL |
| <ul style="list-style-type: none"> • Doctor of Philosophy in Electrical Engineering (w/ GPA of 4.000) • Master of Science in Electrical Engineering (w/ GPA of 4.000) | <p><i>Anticipated Spring 2026</i> <i>Spring 2023</i></p> |
| University of Texas at Dallas | Richardson, TX |
| <ul style="list-style-type: none"> • Master of Science in Electrical Engineering with Computing Systems focus (w/ GPA of 3.959) • Bachelor of Science in Electrical Engineering <ul style="list-style-type: none"> ◦ Summa Cum Laude (w/ GPA of 3.953) ◦ Collegium V Honors • Minor in Nanoscience and Technology | <p><i>Summer 2022</i> <i>Fall 2021</i> <i>Fall 2021</i></p> |

RESEARCH AND WORK EXPERIENCE

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| Physical Electronics Research Laboratory (PERL) Research Assistant | <i>September 2022-Present</i> |
| <ul style="list-style-type: none"> • <i>Supervisor:</i> Dr. Pedram Khalili, PhD • Designed system for performing factorization with VCMA generated random numbers <ul style="list-style-type: none"> ▪ Author on <i>Probabilistic computing with voltage-controlled dynamics in magnetic tunnel junctions</i> | |
| NeuroSpinCompute Laboratory Student Assistant | <i>June 2019-August 2022</i> |
| <ul style="list-style-type: none"> • <i>Supervisor:</i> Dr. Joseph Friedman, PhD • Designed and simulated domain wall systems based neural networks using Virtuoso, Spectre, HSpice and Verilog • Created custom circuit simulation software in Java, and power consumption estimation and graphing software in MATLAB • Coauthored <i>High-Speed CMOS-Free Purely Spintronic Asynchronous Recurrent Neural Network</i> <ul style="list-style-type: none"> ◦ Presented at 2022 Joint MMM-INTERMAG Conference • Designed spintronics neuron represents positive and negative weights with adjustable systematic and random error resistance • Presented at Spring 2020 Undergraduate Research Scholar Award Poster session | |
| Qorvo Product Quality Engineering Intern | <i>May 2021-August 2021</i> |
| <ul style="list-style-type: none"> • Oversaw qualification testing and related material builds for new parts and processes • Redesigned SharePoint database for greater use of use and accuracy • Designed material storage, and processing system to conserve thousands of dollars' worth of materials • Created data processing and analytics scripts | |
| Private Tutor | <i>2017-2020</i> |
| <ul style="list-style-type: none"> • Tutored a variety of clients across grade levels in subjects ranging from elementary math to college level physics | |

ACTIVITIES

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| IEEE Tutor | <i>2020-2022</i> |
| <ul style="list-style-type: none"> • Assists students in understanding concepts of Computer and Electrical Engineering courses | |
| Hackathons | <i>2015-2022</i> |
| <ul style="list-style-type: none"> • Drew knowledge from research papers to create skin wearable electronic controls that interfaced with a smartphone app • Designed from scratch a neural network system trained to solve Minesweeper puzzles • Created software to sort playlists by musical attributes and play music based on detected facial expression • Created a robot that can punt a football using microcontrollers and hardware store supplies | |
| IEEE Eta Kappa Nu President | <i>2020-2021</i> |
| <ul style="list-style-type: none"> • President of newly revived Kappa Kappa chapter of IEEE's academic and service honor society | |
| Society of Automotive Engineers (Formula SAE) | <i>2018-2019</i> |
| <ul style="list-style-type: none"> • Designed and implemented paddle shifting system for use on a formula race car | |

AWARDS & HONORS

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| Texas Analog Center for Excellence Undergraduate Research Internship | <i>January 2020-August 2020</i> |
| University of Texas at Dallas Undergraduate Research Scholars Award | <i>January 2020</i> |
| McKinney School Board of Directors Recognition for Work Teaching Elementary Students Coding | <i>2017</i> |

TECHNICAL SKILLS

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| Programming Languages |
| <ul style="list-style-type: none"> • Verilog, Python (2 & 3), C++, Java, MATLAB, Node.js (w/ socket.io), JavaScript, C, Bash, Assembly, TI-BASIC |
| Software |
| <ul style="list-style-type: none"> • Microsoft Office (esp. Excel), Microsoft Windows, Linux, Git, NI Multisim, Cadence CAD, Virtuoso, Spice, Spectre, Shell |
| Subjects |
| <ul style="list-style-type: none"> • Magnetic Tunnel Junctions, Neural Networks, Digital Design, PCB design, Cloud/Client Programming, Dynamic Programming |
| Design Related Coursework: |
| <ul style="list-style-type: none"> • EE Lab I/II/III, Electromagnetic Engineering, Digital Circuits, Signals & Systems, Systems & Controls, Computer Arch., Senior Design I/II, VLSI Design, ASIC Design, Advanced Digital Logic, Microprocessors & Embedded Systems |
| Nanoscience & Fabrication Related Coursework: |
| <ul style="list-style-type: none"> • Intro. Nanoscience & Nanotechnology, Microscopy Spectroscopy & Nanotech Instrumentation, Electronic Devices, Electronic Circuits, Electronic Optical & Magnetic Materials, Introduction to MEMs, Quantum Mechanics I, Quantum Physics Electronics |